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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/006,976	12/04/2001	Antonio Abbondanzio	RPS920010137US1	3593
7590	07/15/2004		EXAMINER	
Joseph P. Lally DEWAN & LALLY, L.L.P. P.O. Box 684749 Austin, TX 78768-4749			PATEL, NIMESH G	
			ART UNIT	PAPER NUMBER
			2112	

DATE MAILED: 07/15/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/006,976	<b>Applicant(s)</b> ABBONDANZIO ET AL.	
	<b>Examiner</b> Nimesh G Patel	<b>Art Unit</b> 2112	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**.      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-23 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-23 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 04 December 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |   |  |
|---|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                  | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date ____ | 6) <input type="checkbox"/> Other: ____  |

## DETAILED ACTION

### *Specification*

1. Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

### *Claim Rejections - 35 USC § 112*

2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

3. Claims 19-20 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

4. Regarding dependent claims 19-20, the examiner is not clear as to which claims they are dependent since they refer to "claim C" in the first line of each claim. Appropriate Correction is required.

### *Claim Rejections - 35 USC § 102*

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent

granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

6. Claims 1-5, 7-13, 15-20, and 22-23 rejected under 35 U.S.C. 102(e) as being anticipated by McGraw et al.(20020188709), hereinafter referred to as McGraw.

7. Regarding claim 1, McGraw discloses a data processing system, comprising: at least one main processor connected to a system bus(Paragraph 55); a system memory connected to the system bus and accessible to each of the main processors(Paragraph 26); a tamper mechanism configured to change state responsive to insertion of the system into a slot in a rack enclosure(Figure 6); and means for determining system information including geographical address information and for communicating the information externally(Figure 2; Paragraphs 147-149).

8. Regarding claim 2, McGraw discloses a system, wherein the means for determining the geographical address include a local service processor connected to a set of physical identification connector pins indicative of the geographical address of a slot in which the system is inserted(Paragraphs 147-149).

9. Regarding claim 3, McGraw discloses a system, wherein the means for communicating externally comprise a communication bus connected to a local service processor of the system(Figure 2, 131).

10. Regarding claim 4, McGraw discloses a system, wherein the communication bus comprises an RS-485 communication bus to which the local service processor is connected(Figure 7, RS485).

11. Regarding claim 5, McGraw discloses a system, wherein, responsive to a power-on event, a local server processor of the system is configured to determine the system's

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geographical address, the state of its tamper latch, and to communicate the geographical address and tamper latch information externally(Paragraphs 147-149).

12. Regarding claim 7, McGraw discloses a system wherein, responsive to determining that the tamper latch is in an altered state, the service processor is configured to issue an external alert identifying the system by its geographical address(Paragraphs 147-149).

13. Regarding claim 8, McGraw discloses a system, wherein the external alert further identifies the system by system information selected from the list including an identifier of a network interface card of the system, a UUID, and a main processor serial number(Paragraph 137).

14. Regarding claim 9, McGraw discloses a data processing network, comprising: a management module comprising a management module service processor and a memory(Figure 1, 50; It is inherent the console server has a processor and memory); and a plurality of server blades connected to a common network, each blade comprising a system memory connected to at least one main processor(Paragraph 55), a tamper mechanism configured to change state responsive to insertion of the corresponding blade into a slot in a rack enclosure(Figure 6), and means for determining a geographical address of the slot occupied by the blade and for communicating the determined address to the management module(Figure 2; Paragraphs 147-149).

15. \* Regarding claim 10, McGraw discloses a network, wherein the means for determining the geographical address include a local service processor connected to a set of physical identification connector pins indicative of the geographical address of a slot in which the system is inserted(Paragraphs 147-149).

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16. Regarding claim 11, McGraw discloses a network, wherein the means for communicating externally comprise a communication bus connecting a local service processor of each server blade to the management module(Figure 2, 131).

17. Regarding claim 12, McGraw discloses a network, wherein the communication bus comprises an RS-485 communication bus to which each local service processor is connected(Figure 7, RS485).

18. Regarding claim 13, McGraw discloses a network, wherein, responsive to a power-on event, a local server processor of each server blade is configured to determine the blade's geographical address, the state of its tamper latch, and to communicate the geographical address and tamper latch information to the management module(Paragraphs 147-149).

19. Regarding claim 15, McGraw discloses a network, wherein, responsive to determining that the tamper latch is in an altered state, the service processor is configured to issue an external alert to the management identifying the system by its geographical address(Paragraphs 147-149).

20. Regarding claim 16, McGraw discloses a network, wherein the external alert further identifies the system by system information selected from the list including an identifier of a network interface card of the system, a UUID, and a main processor serial number(Paragraph 137).

21. Regarding claim 17, McGraw discloses a network, wherein the management module is configured to communicate the system information to a system deployment module(Figure 1, 72).

22. Regarding claim 18, McGraw discloses a computer program product comprising a set of computer executable instructions for monitoring system information in a data processing network, the instructions being stored on a computer readable medium, comprising: computer

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code means for determining the state of a tamper latch of a data processing system; computer code means for determining the geographical address of the data processing system; computer code means for communicating the tamper latch and geographical address information to a management module connected to the data processing system(Paragraphs 147-149, 155).

23. To analyze claims 19-20, examiner assumes claims 19-20 each depend on claim 18.

24. Regarding claim 19, McGraw discloses a computer program product, wherein the code means for determining the geographical address include code means for a reading a set of physical identification pins of the data processing system, wherein the state of the pins is indicative of the geographical address of a slot in which the system is inserted(Paragraphs 147-149).

25. Regarding claim 20, McGraw discloses a computer program product, wherein the code means for determining the blade's geographical address, the state of its tamper latch, and communicating the geographical address and tamper latch information to the management module is responsive to a power-on event(Paragraphs 147-149).

26. Regarding claim 22, McGraw discloses a computer program product, further comprising code means for issuing an external alert identifying the system by its geographical address responsive to determining that the tamper latch is in an altered state(Paragraphs 147-149).

27. Regarding claim 23, McGraw discloses a computer program product, wherein the external alert further identifies the system by system information selected from the list including an identifier of a network interface card of the system, a UUID, and a main processor serial number(Paragraph 137).



***Claim Rejections - 35 USC § 103***

28. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

29. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

30. Claims 6, 14 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over McGraw, in view of the admitted prior art.

31. Regarding claim 6, McGraw does not specifically disclose a system, wherein the system is configured, responsive to determining that the tamper latch is in an altered state, to configure a functional boot image on the system. However, the admitted prior art discloses a system wherein, a functional boot image is configured on the system (Page 2, Lines 9-13). Therefore, it would have been obvious to configure a functional boot image on the system, as disclosed by the admitted prior art, in the system of McGraw, since a new blade generally does not have an operating system nor persistent data and a boot image would be needed to make the blade functional.

32. Regarding claim 14, McGraw does not specifically disclose a network, wherein each server blade is configured, responsive to determining that its tamper latch is in an altered state, to configure a functional boot image on the server blade. However, the admitted prior art discloses a system wherein, a functional boot image is configured on the system (Page 2, Lines 9-13). Therefore, it would have been obvious to configure a functional boot image on the system, as disclosed by the admitted prior art, in the system of McGraw, since a new blade generally does not have an operating system nor persistent data and a boot image would be needed to make the blade functional.

33. Regarding claim 21, McGraw does not specifically disclose a computer program, further comprising code means for configuring a functional boot image on the server blade responsive to determining that the tamper latch is in an altered state. However, the admitted prior art discloses a system wherein, a functional boot image is configured on the system (Page 2, Lines 9-13). Therefore, it would have been obvious to configure a functional boot image on the system, as disclosed by the admitted prior art, in the system of McGraw, since a new blade generally does not have an operating system nor persistent data and a boot image would be needed to make the blade functional.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nimesh G Patel whose telephone number is 703-305-7583. The examiner can normally be reached on M-F, 8:30-6:00.

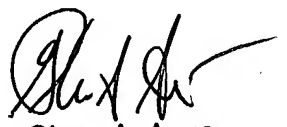
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark H Rinehart can be reached on 703-305-4815. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Nimesh G Patel  
Examiner  
Art Unit 2112

NP NP  
July 8, 2004



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